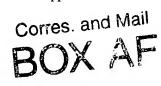


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## REPLY UNDER 37 C.F.R. § 1.116 EXPEDITED PROCEDURE

**EXAMINING GROUP 2183** 

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:

10/044,487

Filed:

January 11, 2002

Inventor(s):

Bodo K. Parady

Title:

LOAD ADDRESS

PREDICTION AND NEW

THREAD

IDENTIFICATION IN A MULTITHREADED MICROPROCESSOR Grou

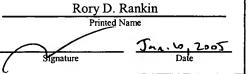
Examiner: Huisman, David J.

Group/Art Unit: 2183

Atty. Dkt. No:

5181-37601

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.



## RESPONSE TO FINAL OFFICE ACTION OF NOVEMBER 26, 2004

999999999999

ATTN: BOX AF

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of November 26, 2004, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.

## IN THE TITLE

Please replace the title with the following new title:

--Load Address Prediction and New Thread Identification in a Multithreaded Microprocessor--